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STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/533,507
Filing Date	05/02/2005
First Named Inventor	M.A.T. Sanduleanu, et
Art Unit	2631
Examiner Name	
Attorney Docket Number	NL 021080

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/J.P./		J. SAVOJ, ET. AL. Design of Half-Rate Clock and Data Recovery Circuits fro Optical Communication Systems; vol. CONF. 38, June 2001; pages 121-126 USA	
/J.P./		CHAN GEUN YOON ET. AL.; Digital Logic Implementation of the Quadricorrelators for Frequency Detector; vol. 2 SYMP 37, August 1994; pages 757-760.	

Examiner Signature	/James Perez/	Date Considered	01/04/2008
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